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24/12/07

<p><b>Substitute form 1449A/PTO</b></p> <p><b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b></p> <p><i>(use as many sheets as necessary)</i></p>				<b>Complete If Known</b>	
				Application Number	10/801,893
				Filing Date	March 16, 2004
				First Named Inventor	Gi-ho Park
				Group Art Unit	2171
				Confirmation No.	3389
				Attorney Docket Number	5649-1159
Sheet	1	of	1		

OTHER NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T
NP	5.	United Kingdom Search Report Under Section 17, United Kingdom Application No. GB0415751.7, November 11, 2004.	
NP	6.	Puttaswamy et al., "System Level Power-Performance Trade-Offs in Embedded Systems Using Voltage and Frequency Scaling of Off-Chip Buses and Memory, " 15 <sup>th</sup> International Symposium on System Synthesis, pp. 225-230 (October 2-4, 2002)	

Examiner Signature  Date Considered 4/16/66

*4/11/05*  
\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.